International application No.
PCT/JP2004/003767

A. CLASSIFICATION OF SUBJECT MATTER Int.Cl <sup>7</sup> H01L25/10, H01L25/18, G06F13/16, G06F12/00, G06F1/1	8, H05K1/02					
According to International Patent Classification (IPC) or to both national classification and IPC						
B. FIELDS SEARCHED						
Int.Cl <sup>7</sup> H01L25/10, H01L25/18, H05K1/02						
Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched  Jitsuyo Shinan Koho 1926-1996 Toroku Jitsuyo Shinan Koho 1994-2004  Kokai Jitsuyo Shinan Koho 1971-2004 Jitsuyo Shinan Toroku Koho 1996-2004						
Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)						
C. DOCUMENTS CONSIDERED TO BE RELEVANT						
Category* Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.					
A JP 2001-177046 A (Hitachi, Ltd.), 29 June, 2001 (29.06.01), Par. Nos. [0023] to [0037], [0071] (Family: none)	1-5,29-36					
A JP 2003-345480 A (Hitachi, Ltd.), 05 December, 2003 (05.12.03), Par. Nos. [0014] to [0025], [0033] (Family: none)	1-5,29-36					
A JP 2000-183173 A (NEC Corp.), 30 June, 2000 (30.06.00), Claim 1; Par. Nos. [0023] to [0043] (Family: none)	1-5,29-36					
See patent family annex.						
* Special categories of cited documents:  "A" document defining the general state of the art which is not considered to be of particular relevance  "T" later document published after the international filing date or date and not in conflict with the application but cited to understone the principle or theory underlying the invention						
"B" earlier application or patent but published on or after the international "X" document of particular relevance; the considered novel or cannot be considered novel or cannot be considered novel or cannot be considered.	sidered to involve an inventive					
"I" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other "Y" document of particular relevance; the	claimed invention cannot be					
special reason (as specified)  "O" document referring to an oral disclosure, use, exhibition or other means  "P" document published prior to the international filing date but later than the priority date claimed combined with one or more other such documents, such being obvious to a person skilled in the art document member of the same patent family						
Date of the actual completion of the international search 22 June, 2004 (22.06.04)  Date of mailing of the international search report 06 July, 2004 (06.07.04)						
Name and mailing address of the ISA/ Japanese Patent Office  Authorized officer						
Facsimile No. Telephone No. Telephone No.						

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Citation of document,  2 11-67970 A  3 March, 1999  3r. Nos. [0017  3 amily: none)	(Fujitsu Lt (09.03.99),	:d.),	te, of the rela	evant passages	1	ant to claim No. 5,29-36
March, 1999 ar. Nos. [0017	(09.03.99),	,				o,∠9-36
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Box No. II	Observations where certain claims were found unsearchable (Continuation of item 2 of first sheet)
1. Clain	nal search report has not been established in respect of certain claims under Article 17(2)(a) for the following reasons:  ns Nos.:  use they relate to subject matter not required to be searched by this Authority, namely:
becau	ns Nos.: use they relate to parts of the international application that do not comply with the prescribed requirements to such an t that no meaningful international search can be carried out, specifically:
3. Claim becan	ns Nos.: use they are dependent claims and are not drafted in accordance with the second and third sentences of Rule 6.4(a).
Box No. III	Observations where unity of invention is lacking (Continuation of item 3 of first sheet)
As mer feature inventi require fourgro of claim	onal Scarching Authority found multiple inventions in this international application, as follows: Intioned on the "extra sheet", there must exist a special technical so linking a group of inventions of claims as to form a single general ve concept in order that the group of inventions may satisfy the ment of unity of invention. The international application contains outsoft inventions: the inventions of claims 1-5 and 29-36; the inventions ms 6-17 and 25-28; the inventions of claims 18-22; and the inventions ms 23-24. tinued to extra sheet.)
clain	
	l searchable claims could be searched without effort justifying an additional fee, this Authority did not invite payment of additional fee.
3. As o	nly some of the required additional search fees were timely paid by the applicant, this international search report covers those claims for which fees were paid, specifically claims Nos.:
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4. X No r	equired additional search fees were timely paid by the applicant. Consequently, this international search report is icted to the invention first mentioned in the claims; it is covered by claims Nos.: 1-5 and 29-36
Remark on P	The additional search fees were accompanied by the applicant's protest.  No protest accompanied the payment of additional search fees.

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## Continuation of Box No.III of continuation of first sheet(2)

- I. Claims 1-5 and 29-36 define an invention of a device comprising mounting board wirings commonly connected to the external terminals of first and second semiconductor devices in a bit-to-bit correspondence and characterized in that "the unevenness of the lengths of the mounting board wirings cancel the unevenness of the lengths of the assembly wirings".
- II. Claims 6-17 and 25-28 define an invention characterized in that a package board "comprises a first conductive layer used for connection to pad electrodes of a semiconductor chip, a second conductive layer used for a ground plane, a third conductive layer used for a power supply plane, and a fourth conductive layer used for connection to a mounting board".
- III. Claims 18-22 define an invention of a technique relating to an electronic circuit that has, on a mounting board, semiconductor memory devices and a semiconductor control device that can conduct access control of the semiconductor memory devices and characterized in that the mounting board has a poser supply plane for a termination power supply for terminating the wiring between the semiconductor memory devices and the semiconductor control device through termination resistors, the semiconductor memory devices are mounted nearer to the power supply plane for the termination power supply than the semiconductor control device, the termination resistors connected to the wiring and first stabilizing capacitors disposed near to the termination resistors are discretely connected to the power supply plane for the termination power supply, and a second stabilizing capacitor having a capacitance than those of the first stabilizing capacitors is connected to the power supply plane for the termination power supply at an edge of the power supply plane far from the supply edge for supplying the termination power".
- IV. Claims 23, 24 define an invention of a semiconductor chip comprising a phase-locked loop circuit or delay locked loop circuit and characterized in that "the first conductive layer has a power supply wiring for supplying power to the phase-locked loop circuit or delay locked loop circuit and a clock wiring for supplying a clock signal to the phase-locked loop circuit or delay locked loop circuit and in that the power supply wiring is spaced from the clock wiring at a distance larger than the minimum spacing dimension of the wiring of the first conductive layer".

There is no technical relationship among the inventions having different features described in I, II, III, and IV and involving one or more of the same or corresponding special technical feature. Therefore, these inventions are not so linked as to form a single general inventive concept. The international application contains four groups of inventions: the inventions of claims 1-5 and 29-36; the inventions of claims 6-17 and 25-28; the inventions of claims 18-22; and the inventions of claims 23, 24.